

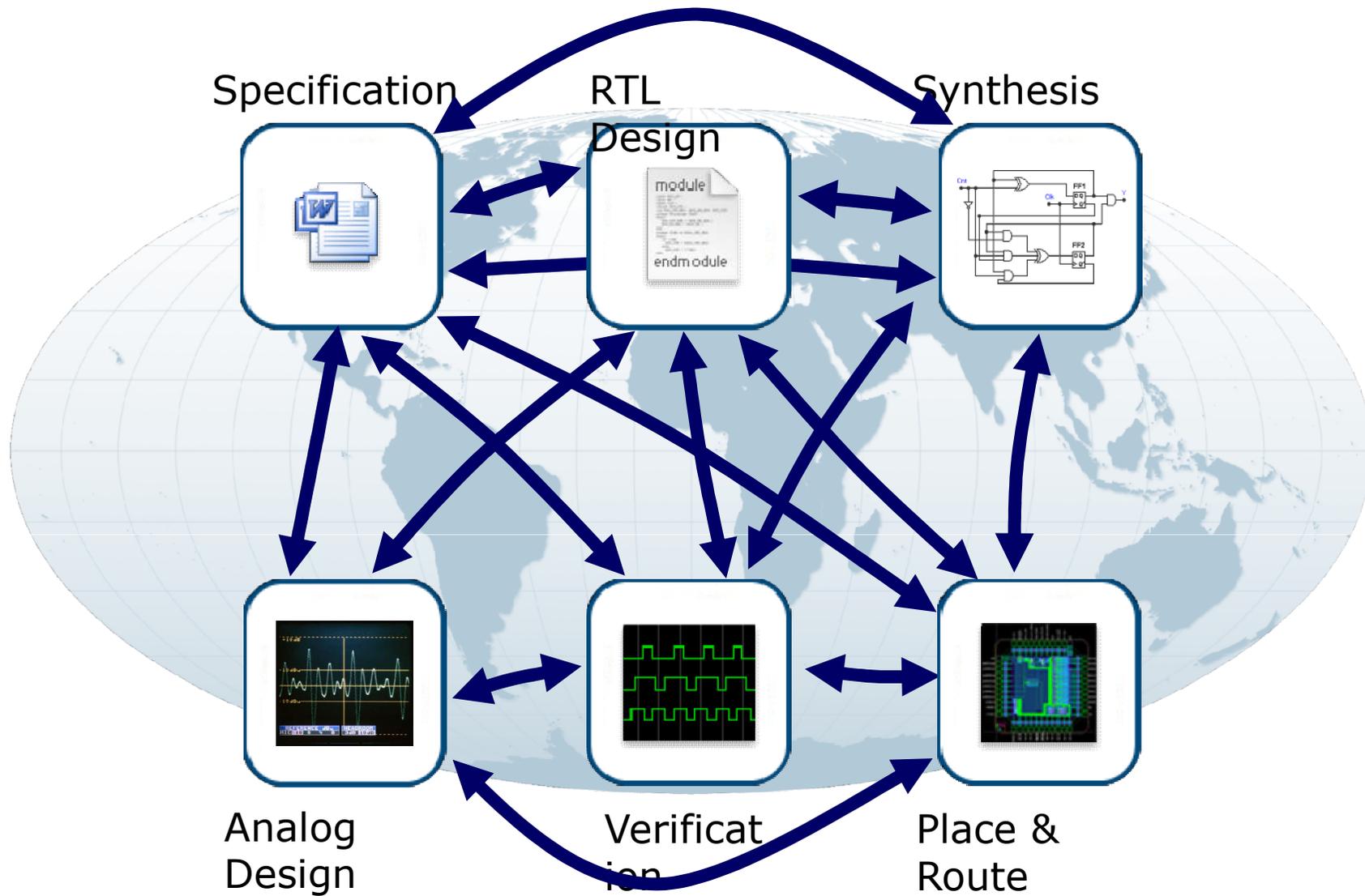
Solutions for Project Management

Michael Henrie
Sr. Engineer
ClioSoft, Inc.

"A story from a customer"

- Multiple designers working on different servers and sharing data between these designers involved tar'ing up and transmitting the data through r-sync/ftp. This was time consuming for designers and the EDA team.
- By opening up permissions for shared libraries there was no accountability and we did not know who was editing what!
- When a designer inadvertently deleted a cell-view it was a hassle to restore his data b/c we had to look in the *.snapshot* directory or the backup tapes.

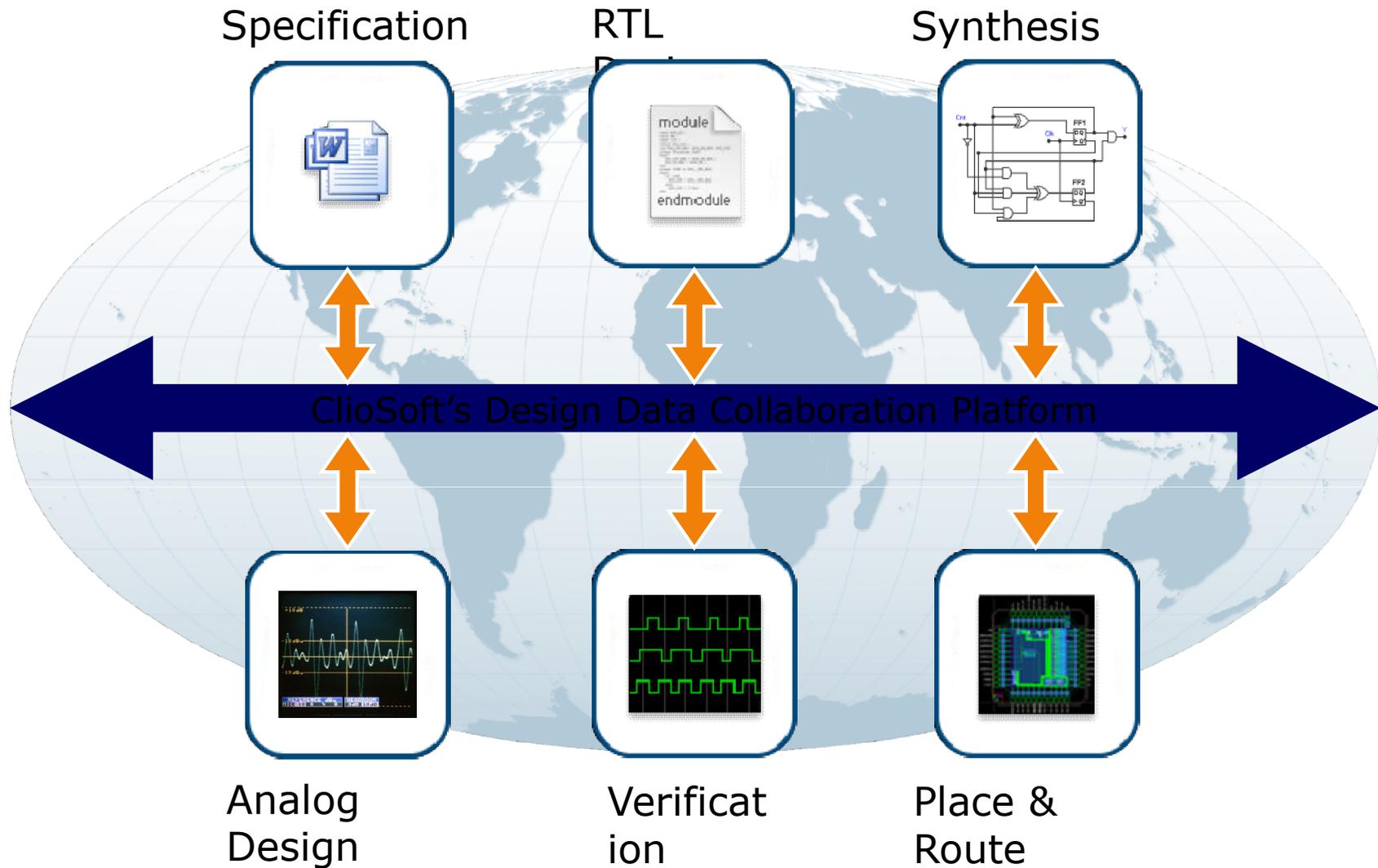
Problems in the Past



Problems in the Past

- Managed ClioSoft projects allow sharing of Cadence libraries and certain files across multiple design teams and sites.
- The Audit trail keeps a detailed log of all activities that took place during the life span of the project.
- ClioSoft permits the retrieval of any checked in cell-view without going to the backup tapes.

Advantages of DM System



Advantages of DM System

- Incorporate Tags and Attributes to verify certain milestones in the design cycle.
- Management can track where most of the time is spent during the design cycle time and figure out the best way to speed up the lagging portion.
- Designers will also benefit since they can utilize tags to organize which Layout cells are DRC/LVS clean or which schematic/config cells they have run advanced analysis on.

Tracking Metrics

SOS-5.40: <no server > @ /home/demo/cliodemo/demo/design

File Project Modify Attrs Select Tree Revision Help

Server: CLIO Project: demo Work Area: /home/demo/cliodemo/demo/design # Selected: 1 # Checked Out: 1

Hierarchy	Locked	% Cover	Change Summary
.			Referenced Cadence startup files from reference proj
Cadence-Virtuoso			Automatically checking in directory.
Mentor-ICstudio			Initial revision.
Sicanvas-Laker			
firmware			Added interface.c and network.c
rtl			Initial revision.
netif.v		82	Merged branch plan_b.
4		94	Power optimization
design_done			
design_verified			
3		82	Merged branch pla
release_1.0			
2		85	Fixed issue 34.
plan_b			
2		73	Fixed race condit
1		55	Try alternative i
1		64	Initial revision.
rxjitbuf.v		89	Timing optimizati
sigmaplus.v	toolman	88	Initial revision.
tdmif.v	sxa	91	Support PCI-X pro
txq.v		88	Fixed protocol er
scripts			Added tcl globals
specs			Initial revision.
tests			Initial revision.
.sosrc			Added plot defaul
.synopsys	enrg		Automate DC setup
protocol_tests			Common protocol t
.cdsinit			Virtuoso startup
cdsLibMgr.il			Cadence Lib Mgr e

Create
Chk Out
Chk In
Tag
Diff

File -> Plot #1

Left Y: line_count Right Y: issue_fixed

From: Start Time: 2004/05/22 17:00:00 To: Now Time: 2004/05/31

Title: Project Stability Metrics. # of Points: 8

Project Stability Metrics.

Date	# Lines	# Issues Fixed
2004/05/22	0	0
2004/05/23	~3000	~15
2004/05/25	~12000	~60
2004/05/26	~21000	~85
2004/05/27	~21000	0
2004/05/28	~19000	~55
2004/05/29	~18000	~30
2004/05/31	~18000	~15

Generate New Dismiss

** Checked out './.synopsys' revision '1'.

© 2007 ClioSoft, Inc. Strictly Confidential

Tracking Metrics